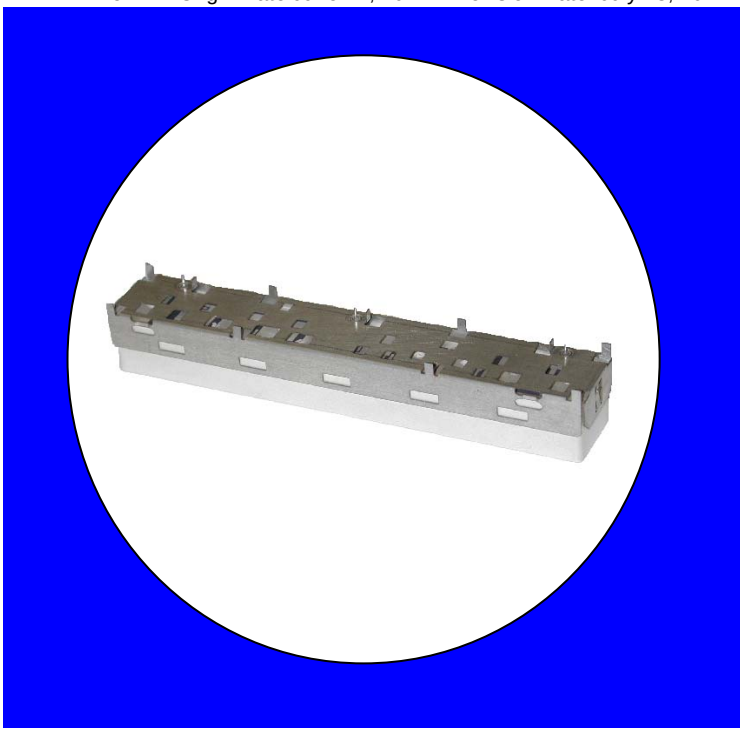
CER0775C

AWS Actiplex® Series Duplexer

Rev A – Origin Date June 27, 2011 - Revision Date: July 28, 2011



Features

- High Power
- Low Insertion Loss
- High Attenuation

Description

Surface mount, silver (Ag) coated ceramic duplexer for use in AWS applications.

Weight: 83 grams typical

Material: Filter is composed of a ceramic block plated with Ag and a Bracket made of nickel silver plated steel.

Filter complies with RoHS standards.

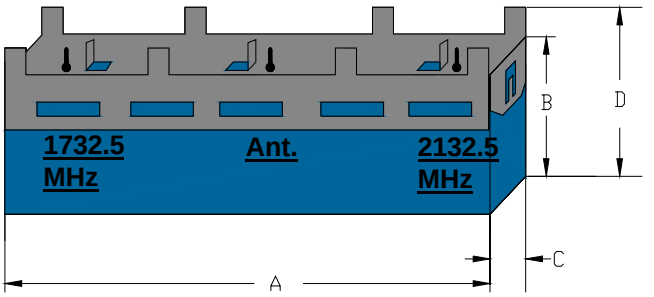
Electrical Specifications

Parameter	Frequency MHz	Typical @ 25ºC	Specification @ 25ºC	Spec over -40ºC to +85ºC
Low Band Response				
Passband Iloss	1710 - 1755	-0.85	-1.30	-1.50
Passband Ripple	1710 - 1755	0.20	0.40	0.60
Passband Return Loss @ Ant	1710 - 1755	-14.00	-12.00	-12.00
Passband Return Loss @ Low Band	1710 - 1755	-14.00	-12.00	-12.00
Attenuation	2110 - 2155	-61.00	-55.00	-55.00
Low Band Response				
Passband Iloss	2110 - 2155	-0.80	-1.30	-1.50
Passband Ripple	2110 - 2155	0.20	0.40	0.60
Passband Return Loss @ Ant	2110 - 2155	-14.00	-12.00	-12.00
Passband Return Loss @ High Band	2110 - 2155	-14.00	-12.00	-12.00
Attenuation	1710 - 1755	-61.00	-55.00	-55.00
Isolation				
Rejection @ Low Band	1710 - 1755	-65.00	-58.00	-58.00
Rejection @ High Band	2110 - 2155	-65.00	-58.00	-58.00
Average Power Atenna to High Band port		50 Watt max		
Peak Power Antenna to High Band Port		200 Watt max		

Note: Supplier shall test each filter to the critical electrical specifications of the above table. Any subsequent audits may deviate from in value due to measurement repeatability among different test systems. Power test will be completed with 50 watts average power in 5 MHz steps across the band. 12 steps total with a 100 millisecond pulse at each frequency point and a 200 watt peak, 1% duty factor with a 9 microsecond pulse. Such deviations shall not exceed the following limits:

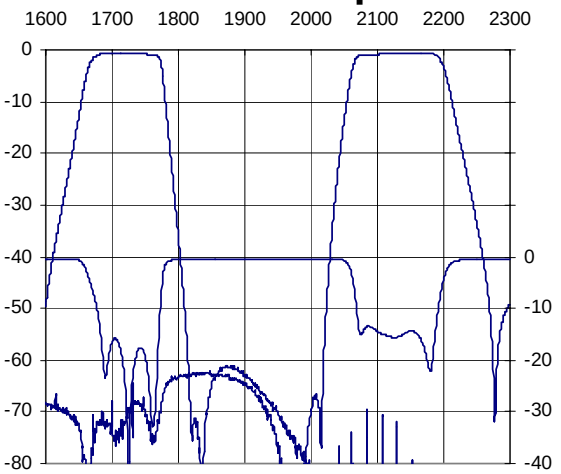
Specification Allowance	
Insertion Loss	0.1 dB
Return Loss	1.0 dB
Stopbands	1.0 dB

Mechanical Drawing

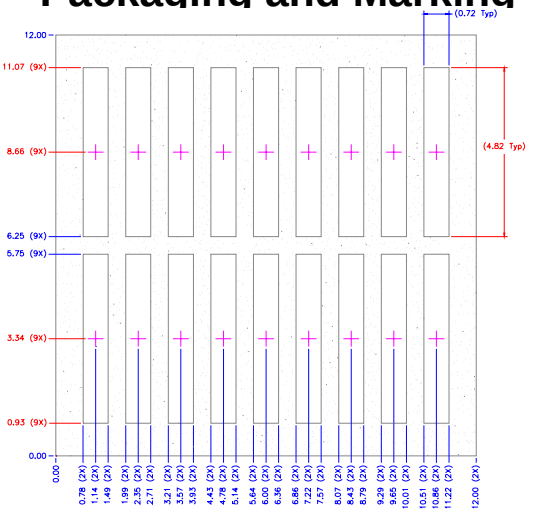


Dim	Nominal (mm)	Tolerance (mm) +/- or max
A	133.10	0.50
B	7.75	0.25
C	19.90	0.25
D	12.70	0.50

Electrical response



Packaging and Marking



PCB Layout

PCB Layout
For additional detail and the latest drawing please contact CTS